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#### 1 A monolithic DC temperature compensation bias scheme for multistage HEMT integrated circuits

Kobayashi, K.W.; Jones, W.L.; MacGowan, K.; Kono, R.; Lee, L.-S.J.;  
Microwave Theory and Techniques, IEEE Transactions on , Volume: 44 , Issue: 2 , Feb. 1996  
Pages:261 - 268

[\[Abstract\]](#) [\[PDF Full-Text \(908 KB\)\]](#) [IEEE JNL](#)

#### 2 A high-speed CMOS amplifier with dynamic frequency compensation

Lee, B.W.; Sheu, B.J.;  
Custom Integrated Circuits Conference, 1990., Proceedings of the IEEE 1990  
16 May 1990  
Pages:8.4/1 - 8.4/4

[\[Abstract\]](#) [\[PDF Full-Text \(320 KB\)\]](#) [IEEE CNF](#)

#### 3 Ku-band low noise MMIC amplifier with bias circuit for compensation of temperature dependence and process variation

Yamanaka, K.; Yamauchi, K.; Mori, K.; Ikeda, Y.; Ikematsu, H.; Tanahashi, N.; Takagi, T.;  
Microwave Symposium Digest, 2002 IEEE MTT-S International , Volume: 3 , 2 June 2002  
Pages:1427 - 1430

[\[Abstract\]](#) [\[PDF Full-Text \(459 KB\)\]](#) [IEEE CNF](#)

#### 4 Class-AB SiC CMOS power opamp with stable voltage gain over wide temperature range

Chen, J.-S.; Kornegay, K.T.;

Circuits, Devices and Systems, IEE Proceedings G-Circuits, Devices and Systems , Volume: 144 , Issue: 1 , Feb. 1997  
Pages:22 - 28

[Abstract] [PDF Full-Text (600 KB)] IEE JNL

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**5 The novel programmable RF predistortion linearizer**

*Jia Sun; Chia, M.Y.W.;*

Microwave Symposium Digest, 2001 IEEE MTT-S International , Volume: 2 , 2 May 2001  
Pages:809 - 812 vol.2

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**6 Low-voltage low-power adaptive biased high-efficiency integrated amplifiers**

*Ferri, G.;*

Electronics, Circuits and Systems, 2001. ICECS 2001. The 8th IEEE International Conference on , Volume: 3 , 2-5 Sept. 2001  
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**7 A variable gain amplifier with 50-dB control range for 900-MHz applications**

*Coffing, D.; Main, E.; Randol, M.; Szklarz, G.;*

Solid-State Circuits, IEEE Journal of , Volume: 37 , Issue: 9 , Sep 2002  
Pages:1169 - 1175

[Abstract] [PDF Full-Text (311 KB)] IEEE JNL

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**8 Performance limitations of low-voltage regulators using only n-p-n transistors**

*Banwell, T.C.;*

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**9 Feedforward compensation techniques for high-frequency CMOS amplifiers**

*Sansen, W.; Chang, Z.Y.;*

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Pages:1590 - 1595

[Abstract] [PDF Full-Text (484 KB)] IEEE JNL

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**10 Offset-compensated low power current comparator**

*Palmisano, G.; Palumbo, G.;*

Electronics Letters , Volume: 30 , Issue: 20 , 29 Sept. 1994  
Pages:1637 - 1639

[Abstract] [PDF Full-Text (236 KB)] IEE JNL

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**11 Push-pull current circuit for biasing CMOS amplifiers with rail-to-rail input common-mode range**

Duque-Carrillo, J.F.; Perez-Aloe, R.; Morillo, A.;  
Electronics Letters , Volume: 27 , Issue: 23 , 7 Nov. 1991  
Pages:2122 - 2125

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**12 A consideration of the compensation method for the gain expansion characteristics of multi-stage amplifiers**

Ikeda, Y.; Mori, K.; Nozawa, T.; Fujii, K.; Takagi, T.;  
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3 , 2-5 Dec. 1997  
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**13 Design of a process variation tolerant CMOS opamp in 6H-SiC technology for high-temperature operation**

Jian-Song Chen; Kornegay, K.T.;  
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**15 Linearized InGaP/GaAs HBT MMIC power amplifier with active bias circuit**

Noh, Y.; Park, C.S.;  
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Pages:249 - 252

[Abstract] [PDF Full-Text (272 KB)] IEEE CNF

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Serial	Document ID	Issue Date	Pages	Title	Current ORI	Current XRef	Retrieval Classif	Inventor	ASL	CCE	EPB	SL	US
1	US 6766369 B1	20040727	16	Threshold voltage compensation	327/534			Dunn, Daniel J. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
2	US 6466090 B1	20021015	33	Digitally programmable continuous-time modules for signal processing	330/96	327/532; 330/69		Gurrola, Horia	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
3	US 6037807 A	20000314	12	Synchronous sense amplifier with temperature and voltage compensated translator	327/52	327/362; 327/513		Wu, Chau-Chin et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
4	US 5140279 A	19920818	11	Slow rate enhanced linear amplifier	330/156	330/260; 330/294		Scott, III, Baker P. L.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
5	US 4912427 A	19900327	17	Power supply noise rejection technique for amplifiers	330/257	330/149		Rybicki, Mathew A.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
6	US 4497586 A	19850205	7	Calculus electronic thermometer circuit	374/163	374/178		Nelson, Carl T.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>